

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT		Docket Number: 2885/86	Confirmation Number: 9148
Application Number 10/501,845	Filing Date August 26, 2005	Examiner Keith E. Vicary	Art Unit 2183
Invention Title RECONFIGURABLE GENERAL PURPOSE PROCESSOR HAVING TIME RESTRICTED CONFIGURATIONS		Inventors Martin VORBACH et al.	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being electronically
transmitted to the United States Patent and Trademark Office on
Date: November 3, 2011
Signature: /Eunice Kim/
Eunice Kim

Sir:

Pursuant to 37 CFR § 1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached List of References. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom. The filing of this Information Disclosure Statement and the enclosed List of References, shall not be construed as an admission that the information cited is prior art, or is considered to be material to patentability as defined in 37 C.F.R. § 1.56(b). The paragraphs marked below are applicable. It is believed that no fees other than those indicated below are due, but authorization is hereby given to charge any additional fees due, or to credit any overpayment, to **Kenyon & Kenyon LLP, deposit account 11-0600.**

☒ 1. This Information Disclosure Statement is being filed (a) within three months of the filing date of a national application other than a continued prosecution application under 37 C.F.R. §1.53(d), (b) within three months of the date of entry of the national stage as set forth in 37 C.F.R. § 1.491 in an international application, (c) before the mailing date of a first Office Action on the merits in the present application, OR (d) before the mailing of a first office action after filing of a request for continued examination. No certification or fee is required.

☐ 2. This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a final action, Notice of Allowance, or any action that otherwise closes prosecution.

☐ a. I hereby certify that each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(1).

☐ b. I hereby certify that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to my knowledge after making reasonable inquiry,

was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(2).

☐ c. The required fee of **\$180.00** under 37 CFR §1.17(p) is being paid by **credit card** to ensure consideration of the disclosed information. Any additional fees may be charged to Deposit Account No. 11-0600 of Kenyon & Kenyon LLP

☒ 3. Since this application was filed after June 30, 2003, copies of U.S. references are not included.

Respectfully submitted,

Date: November 3, 2011

/Aaron Grunberger/
Aaron Grunberger
Reg. No. 59,210

KENYON & KENYON LLP
One Broadway
New York, NY 20004
(212) 425-7200 telephone
(212) 425-5288 facsimile

CUSTOMER NUMBER 26646

INFORMATION DISCLOSURE STATEMENT BY APPLICANTS List of References	Attorney Docket No. 2885/86	Serial No. 10/501,845
	Applicant(s) Vorbach et al.	
	Filing Date August 26, 2005	Group Art Unit 2183

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT/ PUBLICATION NUMBER	PATENT/PUBLICATION DATE	NAME	CLASS	SUBCLASS	FILING DATE
	3,564,506	February 16, 1971	Bee et al.			
	4,873,666	October 10, 1989	Lefebvre et al.			
	6,321,298	November 20, 2001	Hubis			
	4,939,641	July 3, 1990	Schwartz et al.			
	5,119,290	June 2, 1992	Loo et al.			
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	5,584,013	December 10, 1996	Cheong et al.			
	5,717,890	February 10, 1998	Ichida et al.			
	5,915,099	June 22, 1999	Takata et al.			
	2003/0056062	March 20, 2003	Prabhu			
	2006/0036988	February 16, 2006	Allen et al.			
	7,873,811	January 18, 2011	Wolinski et al.			
	5,493,663	February 20, 1996	Parikh			
	5,574,927	November 12, 1996	Scantlin			
	5,784,630	July 21, 1998	Saito et al.			
	5,838,988	November 17, 1998	Panwar et al.			
	6,154,826	November 28, 2000	Wulf et al.			
	6,859,869	February 22, 2005	Vorbach			

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS

EXAMINER'S INITIALS	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.	
	Altera, "Implementing High-Speed Search Applications with Altera CAM," July 2001, Ver. 2.1, Application Note 119, 50 pages.	
	Bolsens, Ivo (CTO Xilinx), "FPGA, a history of interconnect," Xilinx slide presentation, posted on the internet October 30, 2008 at http://www.docstoc.com/docs/2198008/FPGA-a-history-of-interconnect , 32 pages.	
EXAMINER	/Keith Vicary/	DATE CONSIDERED 12/27/2011
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		